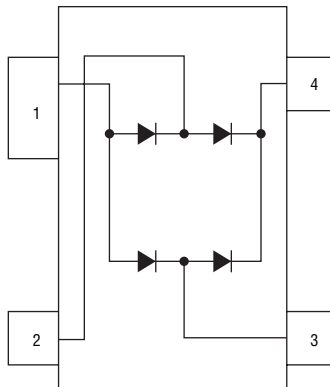


General Information

The CD143A-SR70 device provides ESD protection for the external ports of portable electronic devices such as cell phones, handheld electronics and personal computers. The device also provides EFT and surge protection.

The ESD protection provided by the component enables a data port to withstand a minimum ± 8 KV Contact / ± 15 KV Air Discharge per the ESD test method specified in IEC 61000-4-2. The device measures 2.80 mm x 1.20 mm and is available in a SOT-143 package intended to be mounted directly onto an FR4 printed circuit board.



Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Min.	Nom.	Max.	Unit
Capacitance @ 0 V 1 MHz*	C _j			10	pF
Repetitive Peak Reverse Voltage				70	V
Reverse Breakdown Voltage I @ 50 μA	V _{BR}	85			V
Forward Clamping Voltage 8/20 μs I _{PP} @ 1 A	V _{FC}			1.5	V
Forward Clamping Voltage 8/20 μs I _{PP} @ 24 A	V _{FC}			7	V
Reverse Leakage Current @ V _{RRM}	I _R			1	μA
ESD Protection: IEC 61000-4-2 Contact Discharge Air Discharge		± 8 ± 15			kV kV
EFT Protection: IEC 61000-4-4 @ 5/50 ns		40			A
Surge Protection: IEC 61000-4-5 @ 8/20 μs Level 2 (Line-Gnd) & Level 3 (Line-Line)		24			A

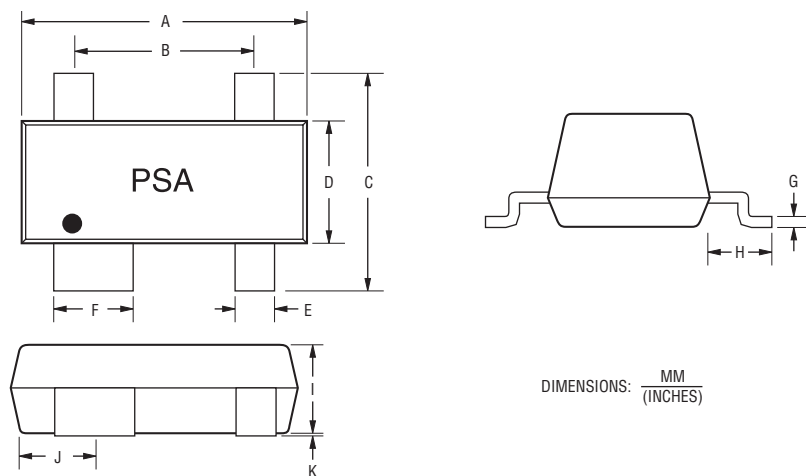
*Measured between I/O pins and ground. 3 pF typical between I/O pins.

Thermal Characteristics (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Min.	Nom.	Max.	Unit
Forward Peak Pulse Current (tp = 8/20 μs)	I _{PP}		24		A
Operating Temperature Range	T _J	-55	25	+150	$^\circ\text{C}$
Storage Temperature Range	T _{STG}	-55	25	+150	$^\circ\text{C}$

Product Dimensions

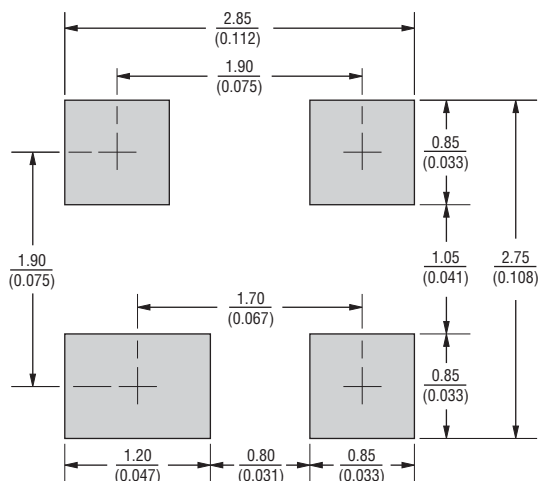
This is a molded JEDEC SOT-143 device. It weighs approximately 35 mg and has a flammability rating of UL 94V-0. The dimensions for the packaged device are shown below.



Dimensions	
A	$\frac{2.80 - 3.04}{(0.110 - 0.12)}$
B	$\frac{1.78 - 2.03}{(0.070 - 0.080)}$
C	$\frac{2.11 - 2.48}{(0.083 - 0.098)}$
D	$\frac{1.20 - 1.39}{(0.047 - 0.055)}$
E	$\frac{0.39 - 0.50}{(0.015 - 0.020)}$
F	$\frac{0.79 - 0.93}{(0.031 - 0.037)}$
G	$\frac{0.08 - 0.15}{(0.003 - 0.006)}$
H	$\frac{0.46 - 0.60}{(0.018 - 0.024)}$
I	$\frac{0.84 - 1.14}{(0.033 - 0.045)}$
J	$\frac{0.72 - 0.83}{(0.028 - 0.033)}$
K	$\frac{0.013 - 0.10}{(0.0005 - 0.004)}$

Recommended Pad Layout

This is the footprint recommended for this SOT-143 device.



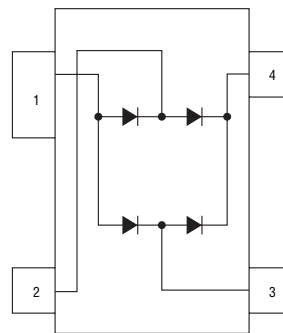
DIMENSIONS: $\frac{\text{MM}}{(\text{INCHES})}$

Typical Part Marking

CD143A-SR70.....PSA

Block Diagram

The device block diagram below includes the pin names and basic electrical connections associated with each channel.



Device Pin Out

Pin	Function
1	V_N
2	I/O 1
3	I/O 2
4	V_P